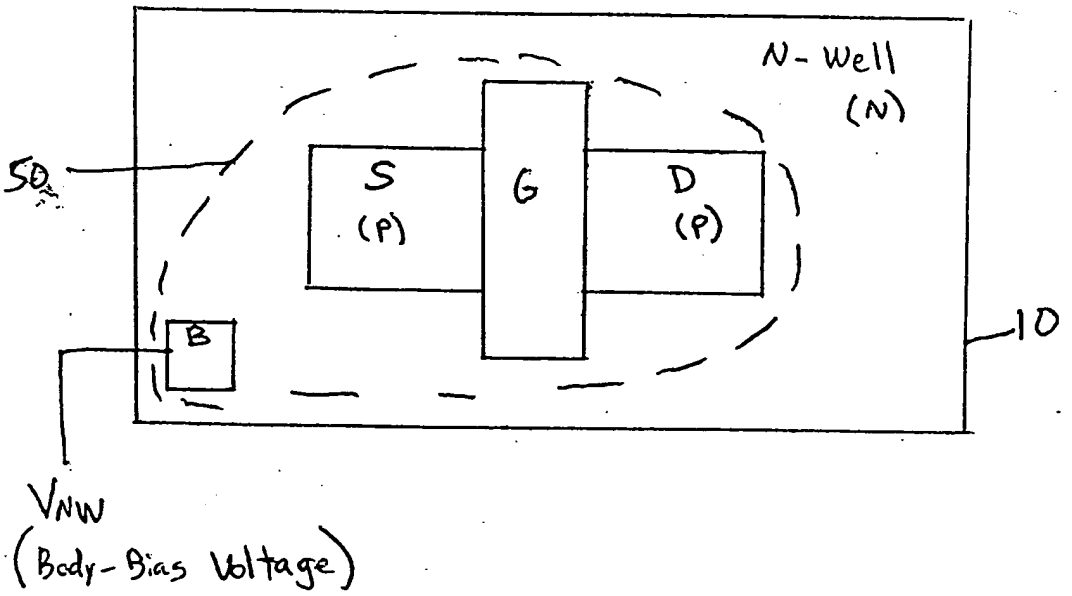
Figure 1



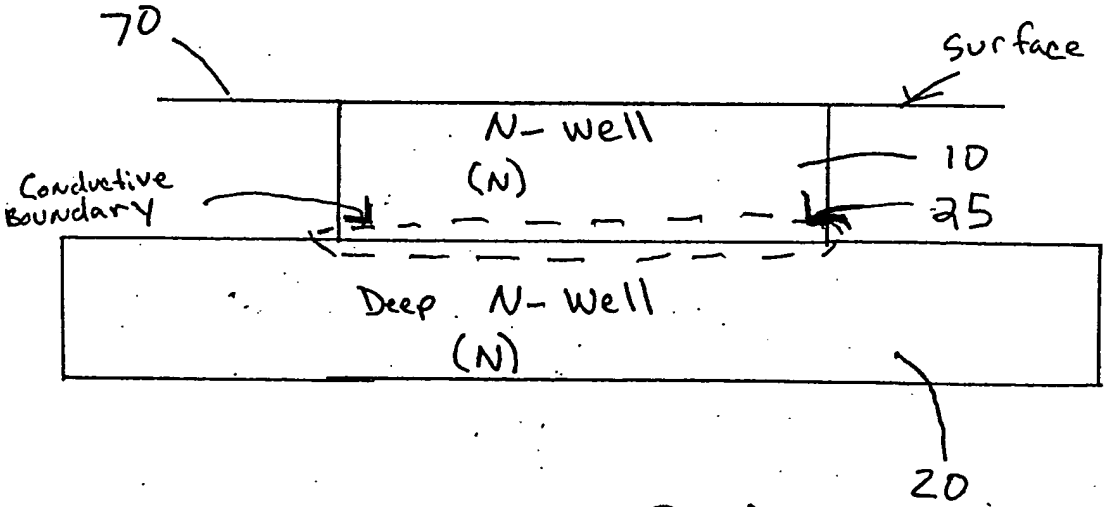


Figure 2A

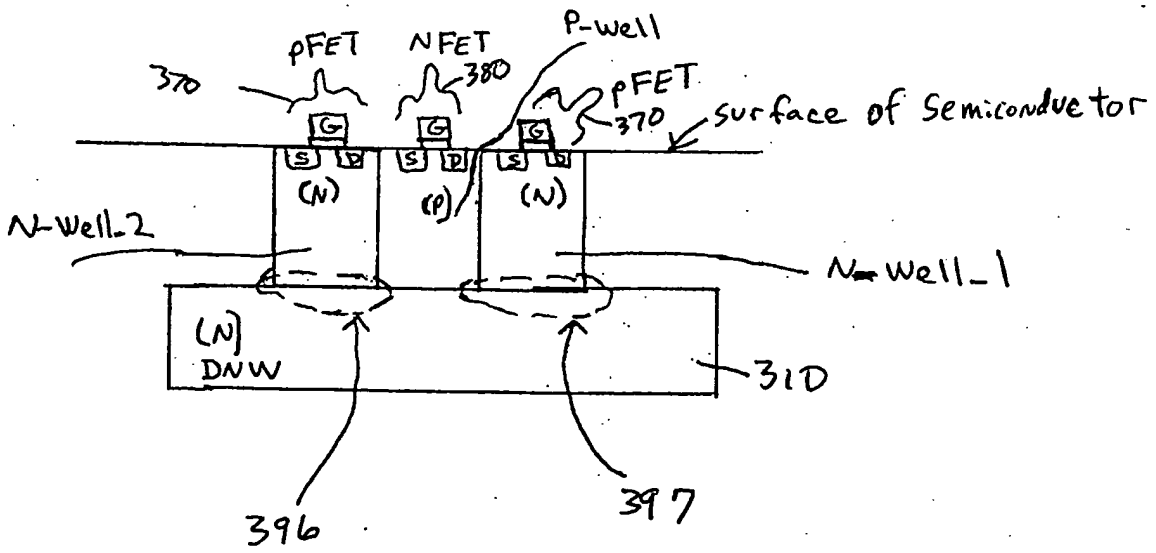

pFET
NFET
P-well
370
380
pFET
370
surface of Semiconductor
G
S
D
(N)
(P)
(N)
N-well_2
N-well_1
(N)
DNW
310
396
397

Figure 2B

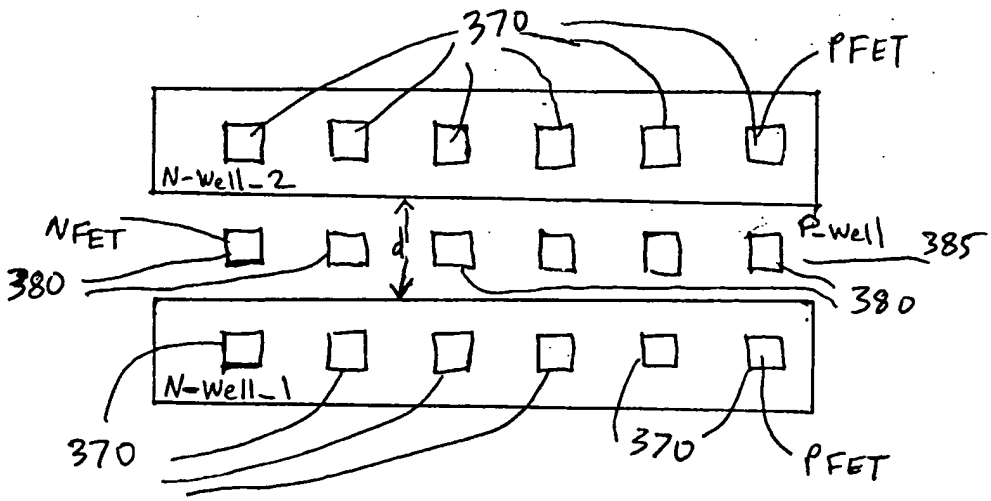


Figure 3A

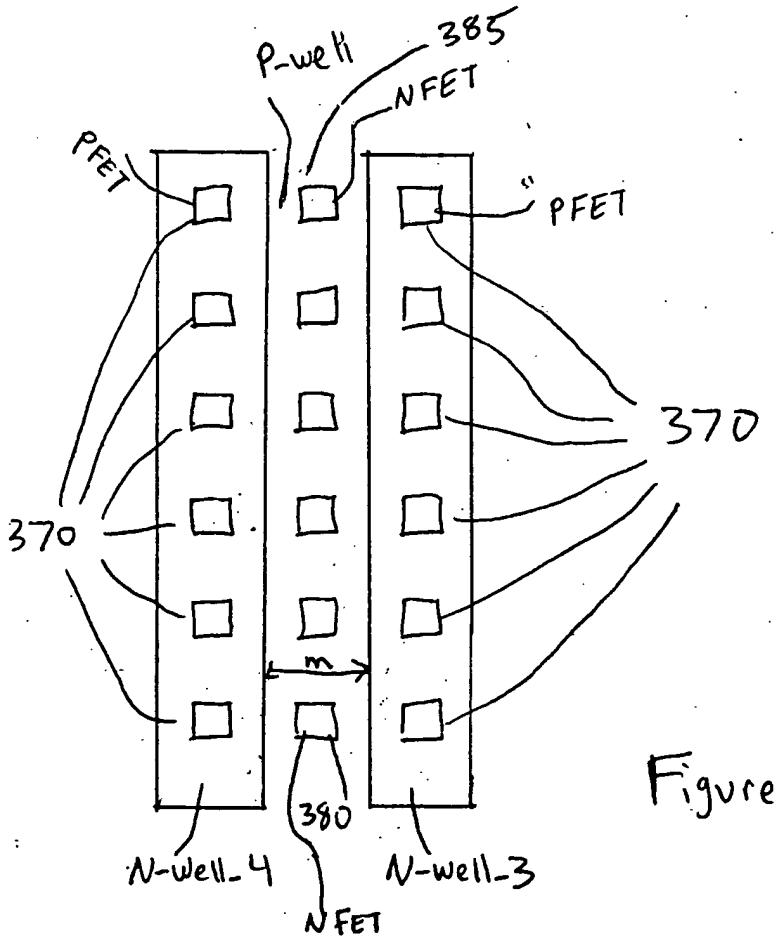


Figure 3B

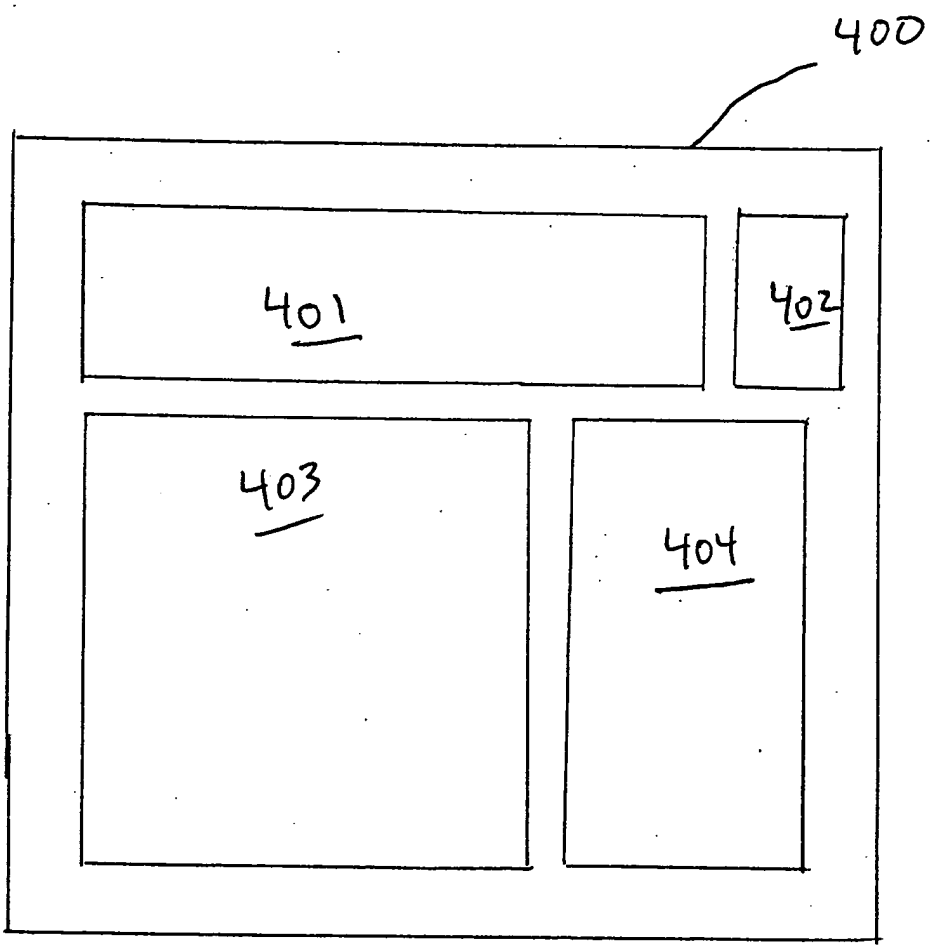


Figure 4

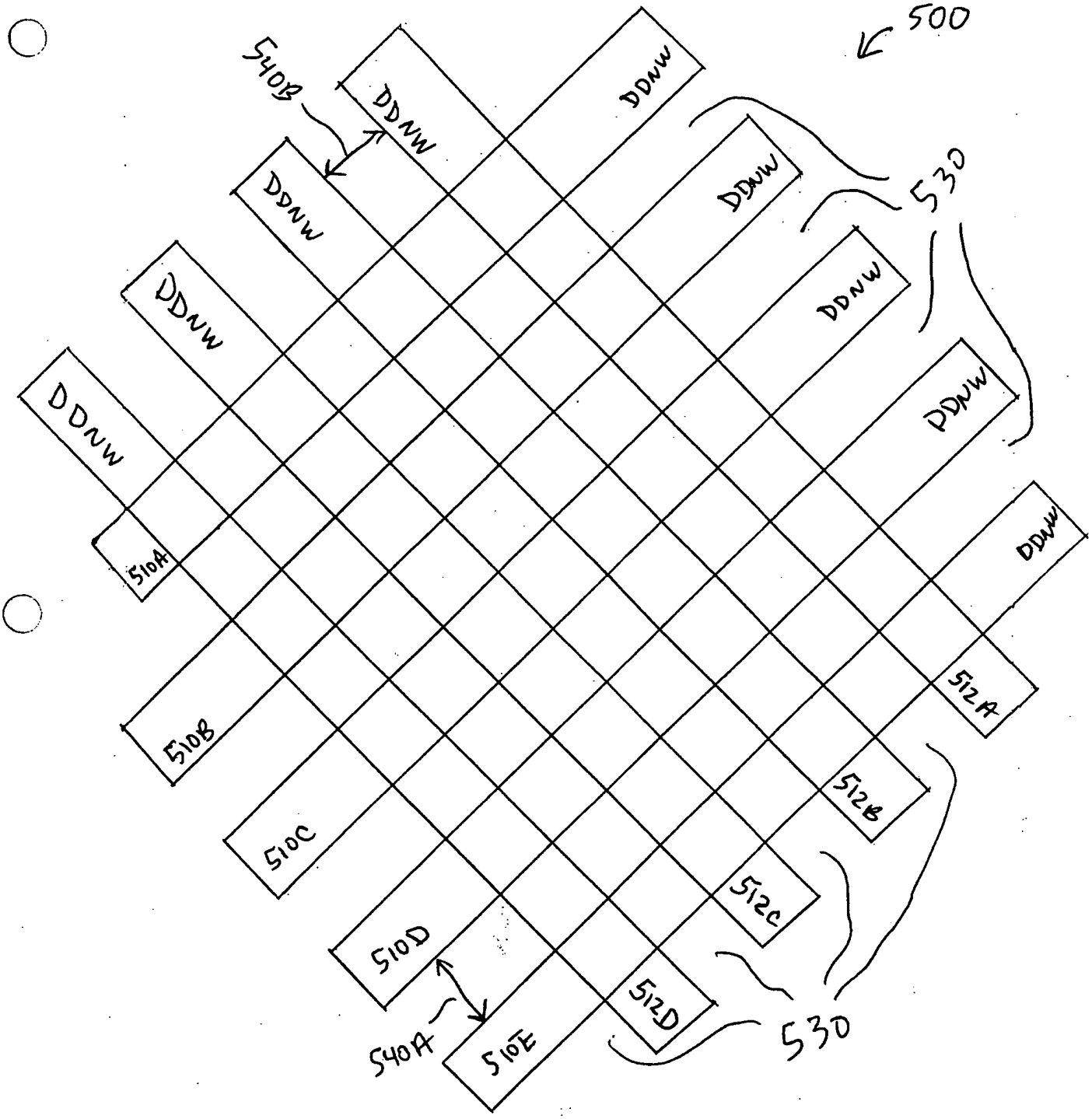

500
540B
DDNW
530
510A
510B
510C
510D
510E
512A
512B
512C
512D
540A
530

Figure 5A

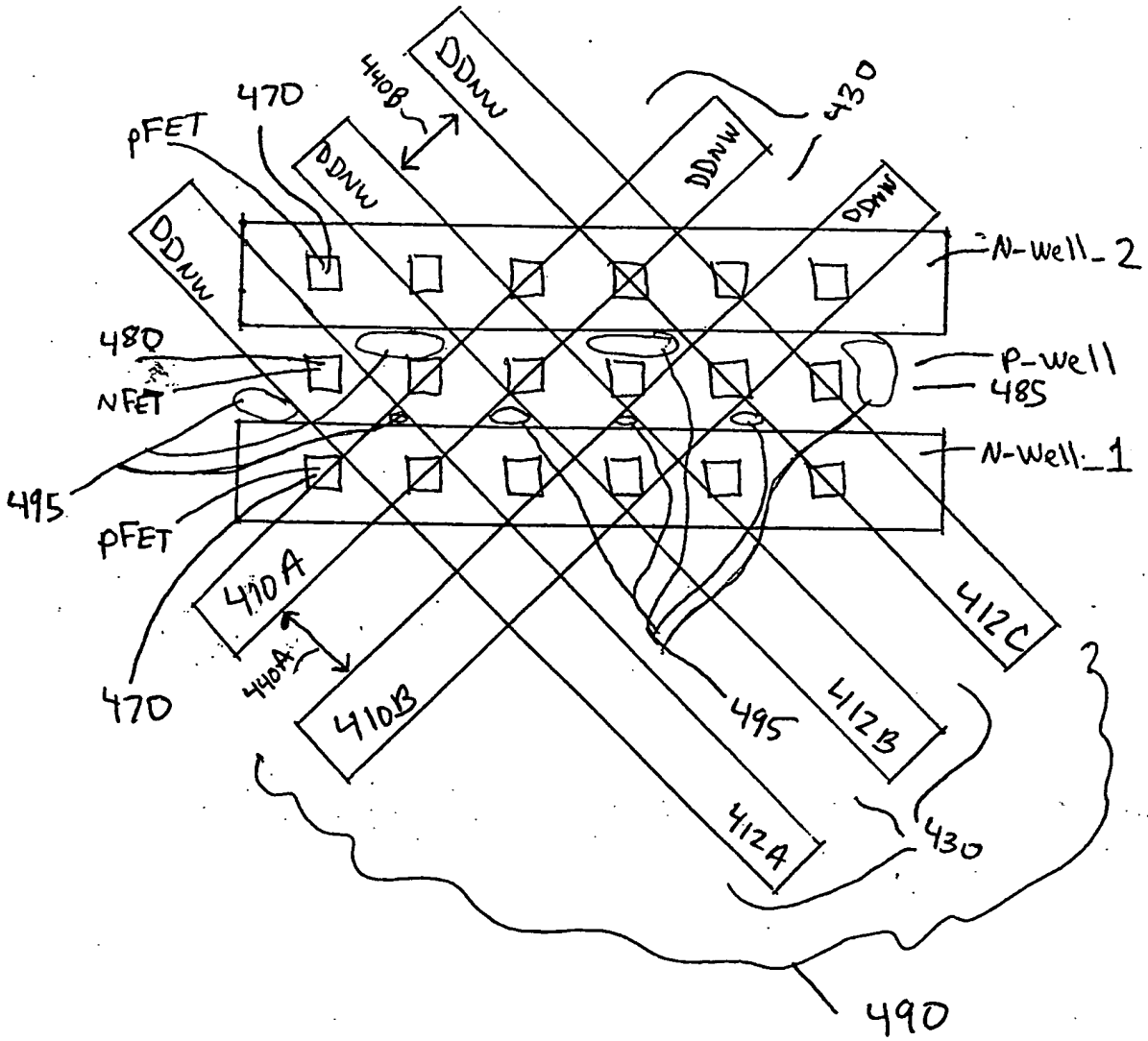

DDNW
DDNW
DDNW
DDNW
DDNW
440B
430
470
pFET
N-well_2
480
NFET
P-well
485
N-well_1
495
pFET
410A
440A
412C
470
410B
495
412B
412A
430
490

Figure 5B

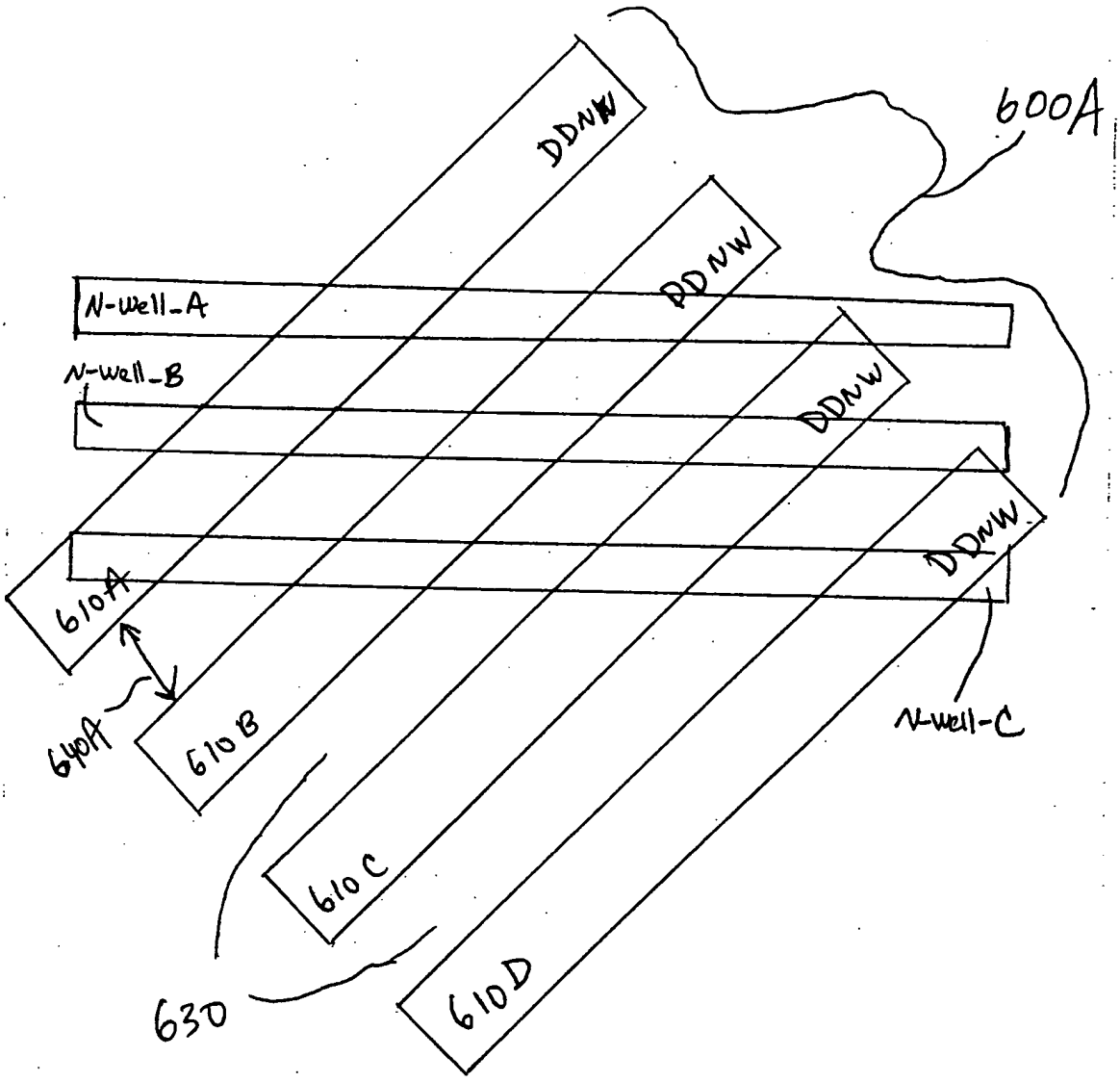

600A
DDNW
DDNW
DDNW
DDNW
N-well_A
N-well_B
N-well-C
610A
610B
610C
610D
640A
630

Figure 6A

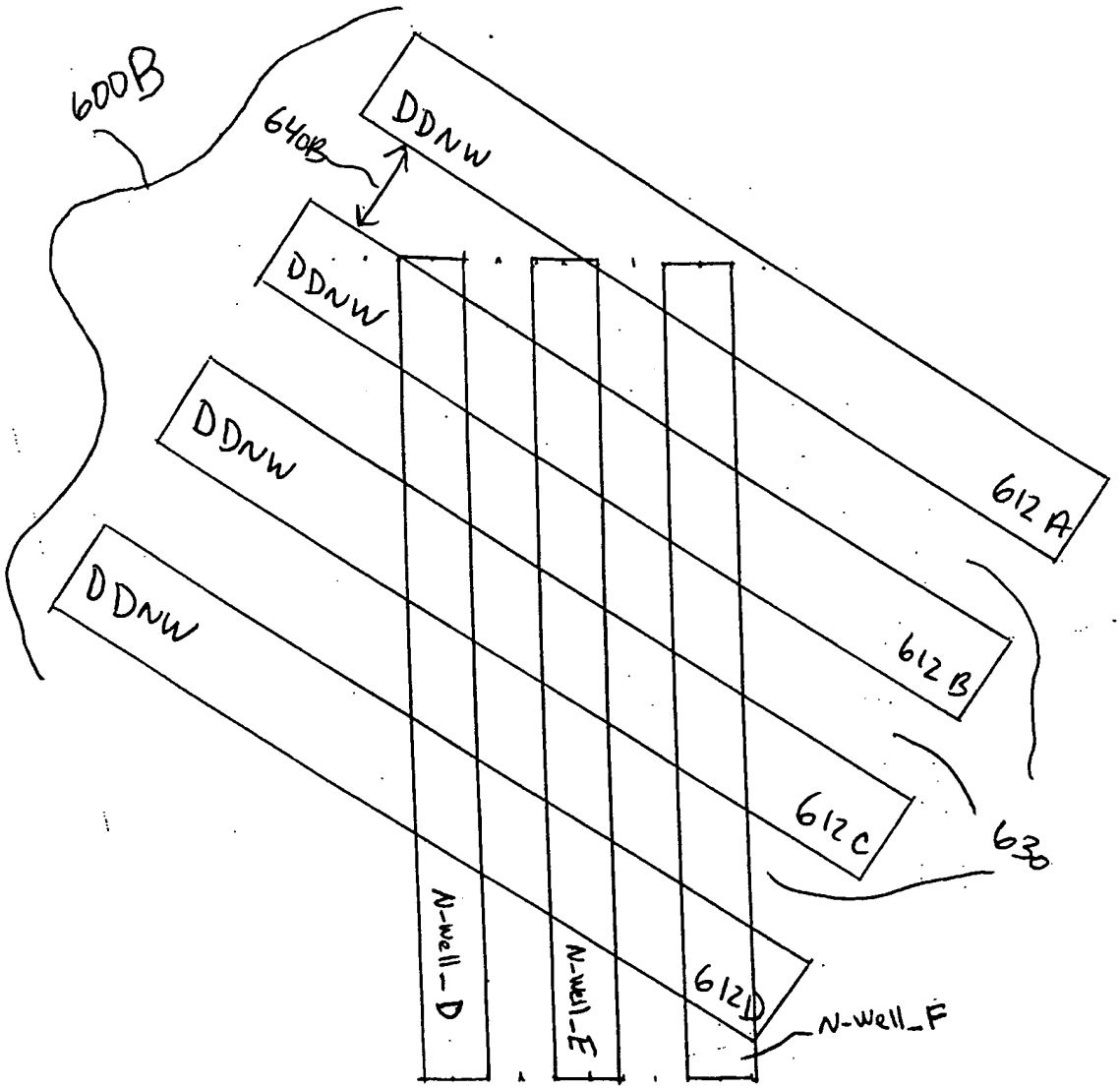

600B
640B
DDNW
DDNW
DDNW
DDNW
612A
612B
612C
612D
630
N-well_D
N-well_E
N-well_F

Figure 6B

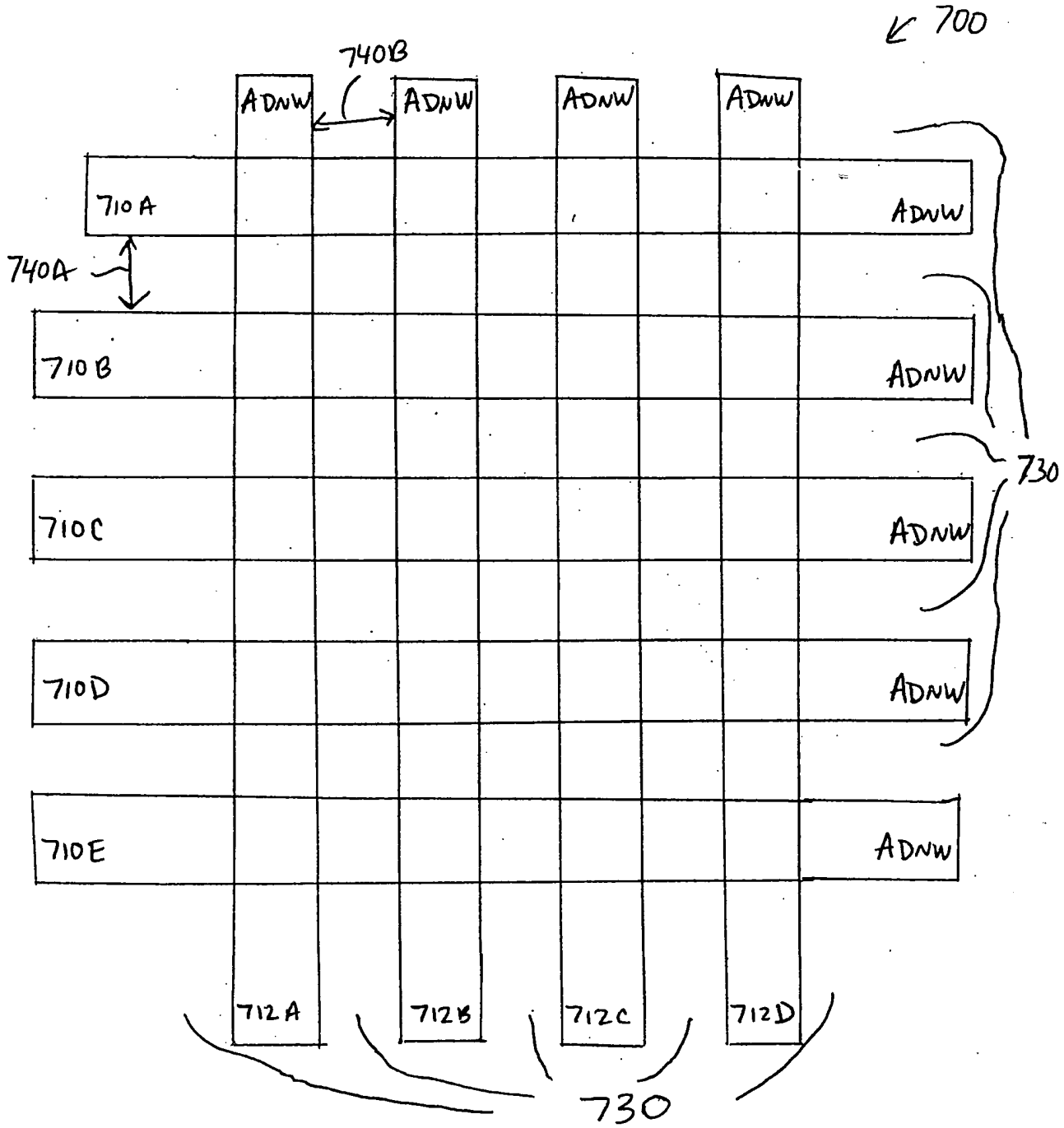

700
740B
ADNW
ADNW
ADNW
ADNW
710A
ADNW
740A
710B
ADNW
730
710C
ADNW
710D
ADNW
710E
ADNW
712A
712B
712C
712D
730

Figure 7

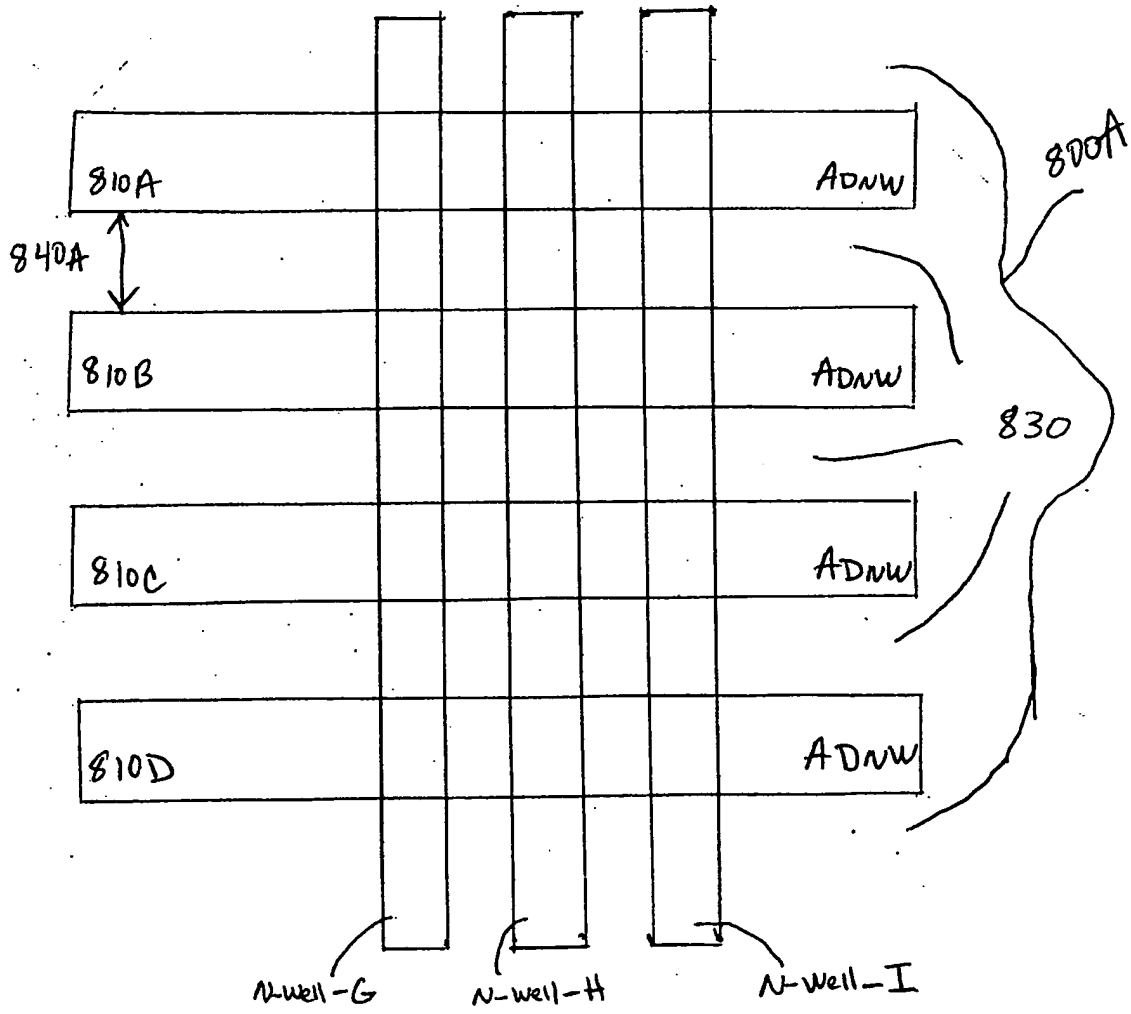

810A
ADNW
840A
810B
ADNW
830
810C
ADNW
810D
ADNW
800A
N-well-G
N-well-H
N-well-I

Figure 8A

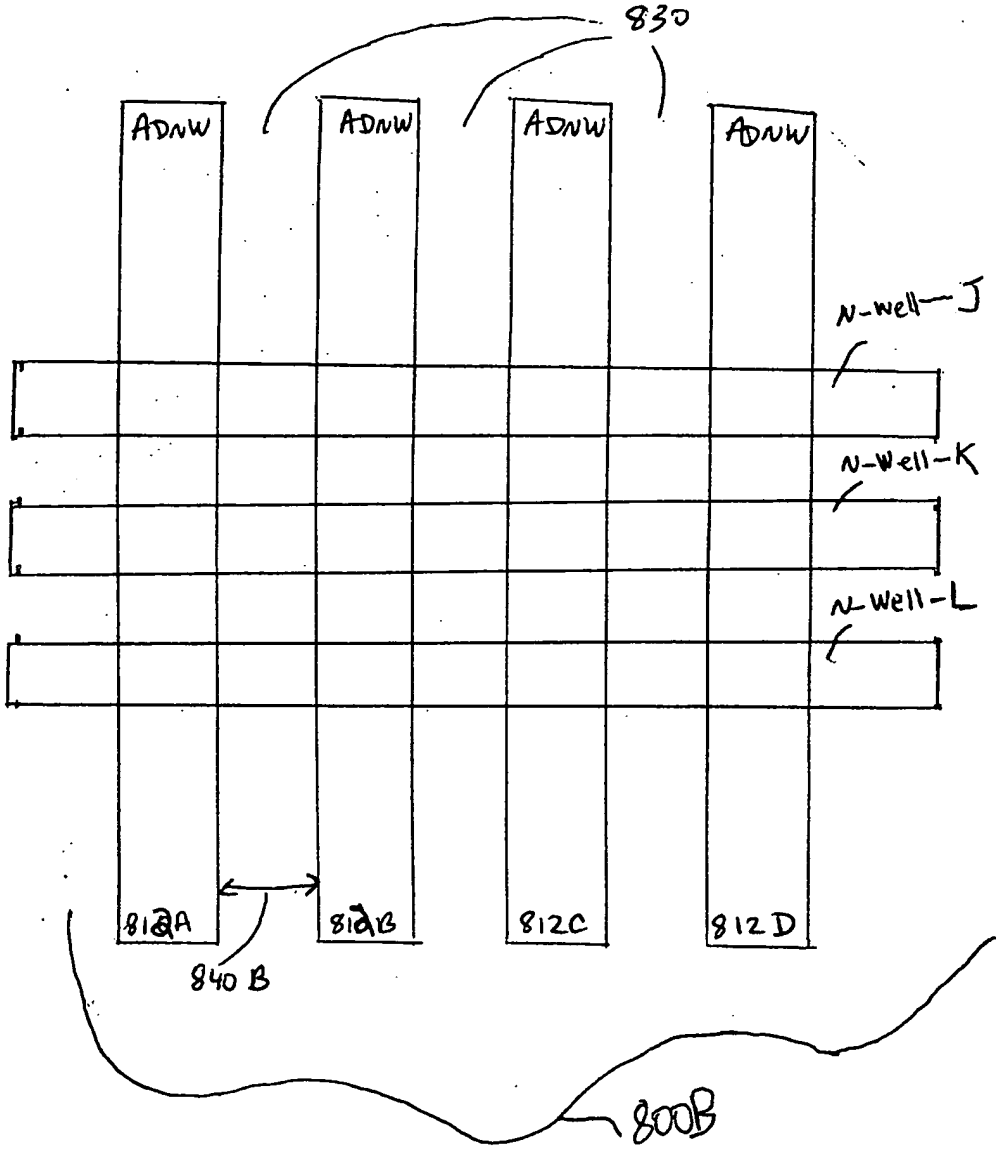

830
ADNW
ADNW
ADNW
ADNW
N-well—J
N-Well-K
N-Well-L
812A
812B
812C
812D
840B
800B

Figure 8B